

FIG.1

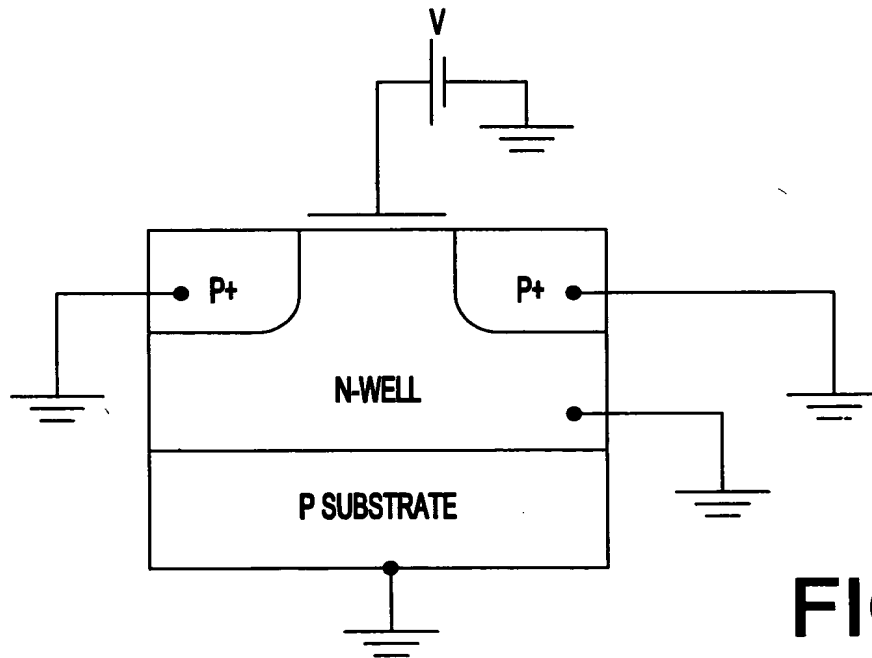


FIG.2

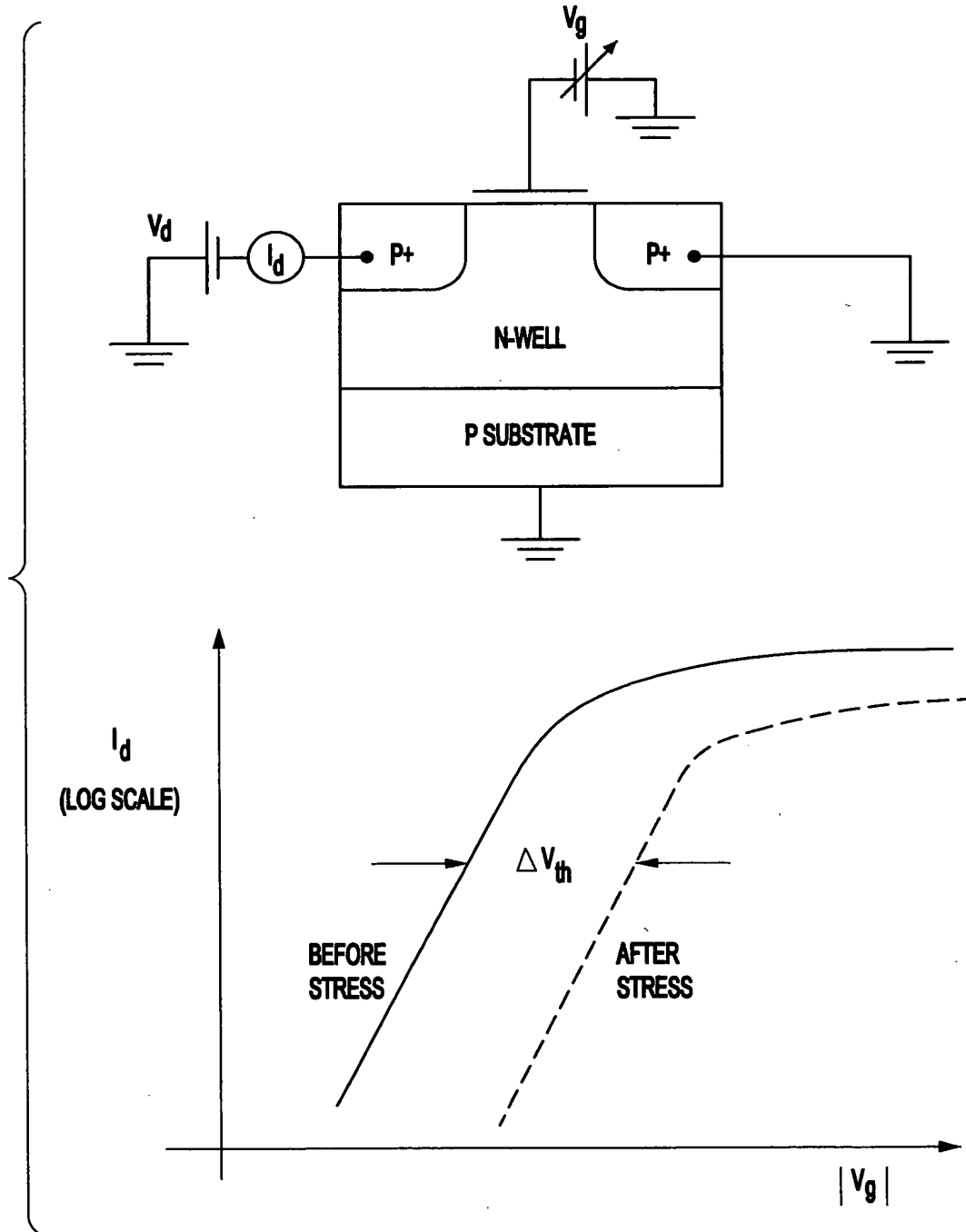


FIG.3



FIG.4

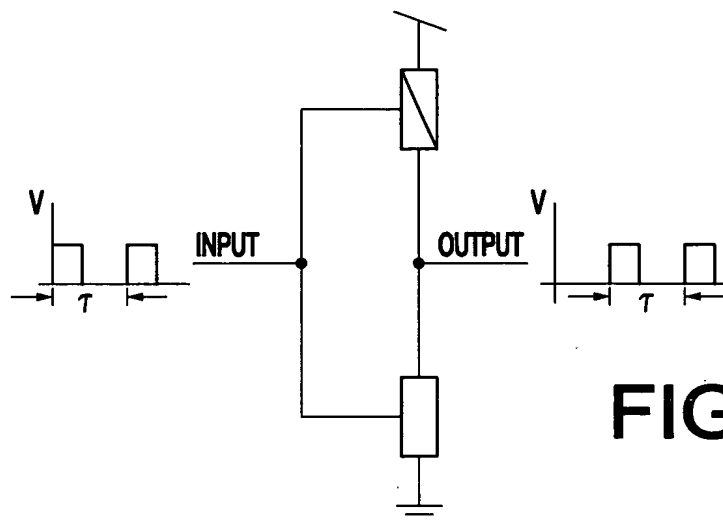
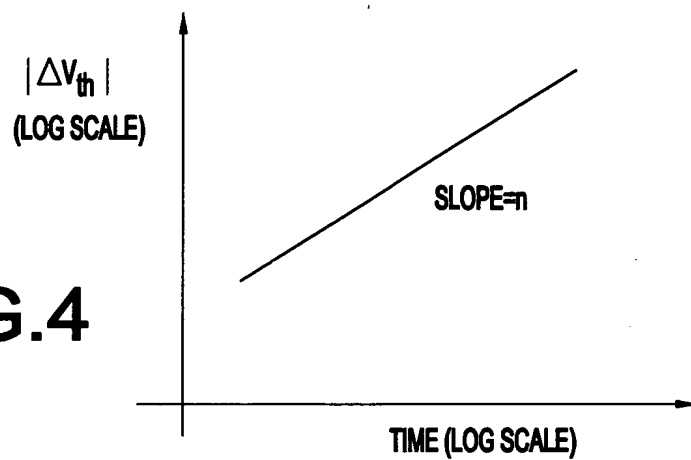


FIG.5

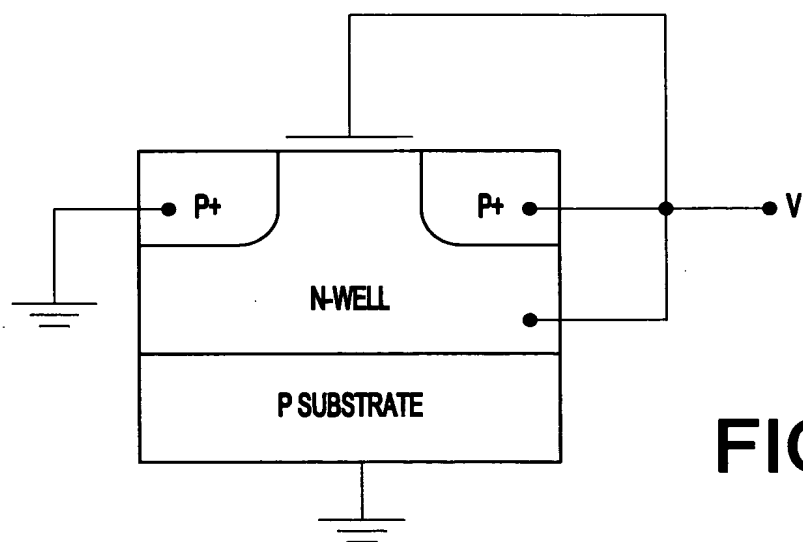


FIG.6

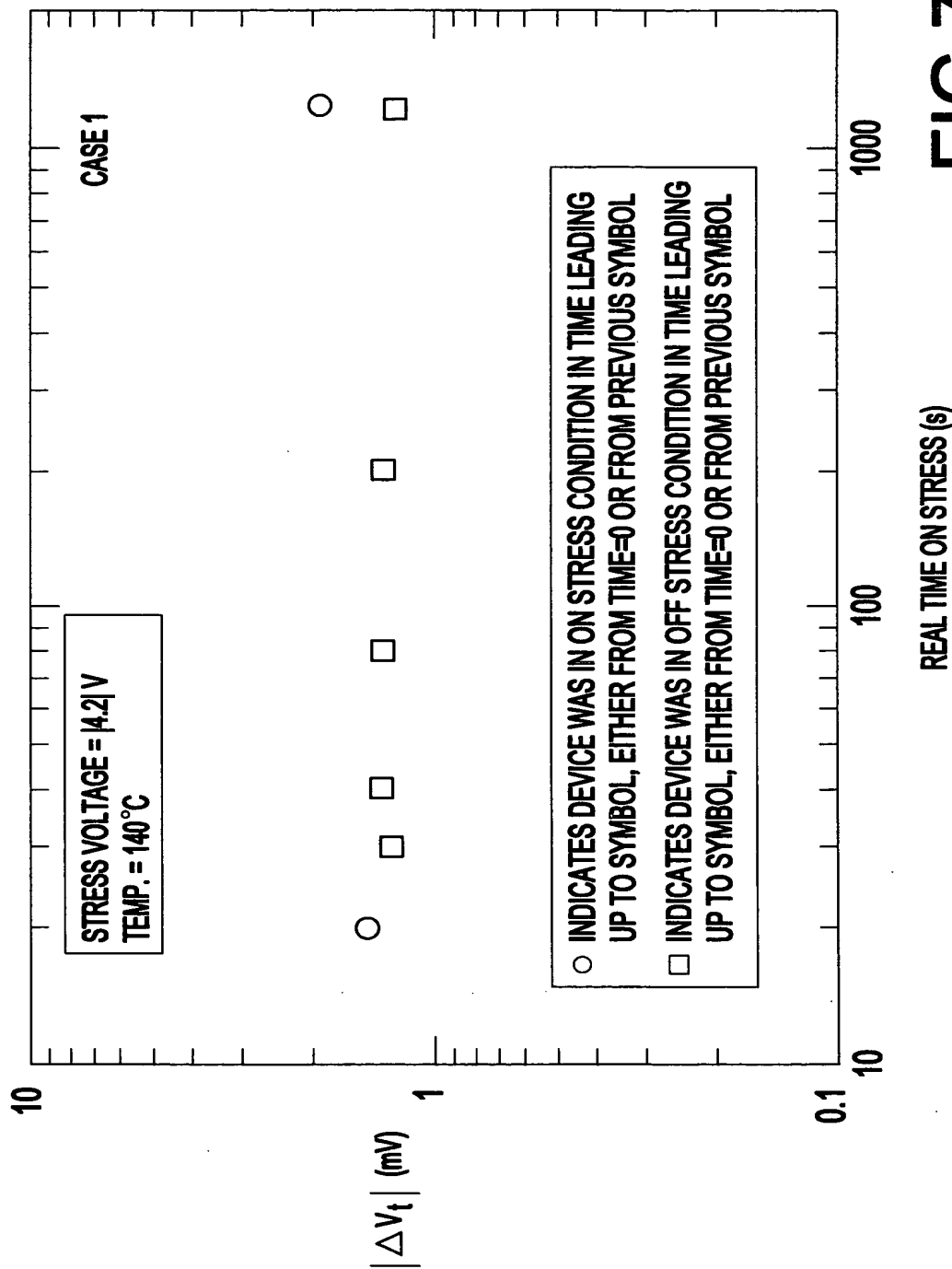


FIG.7A

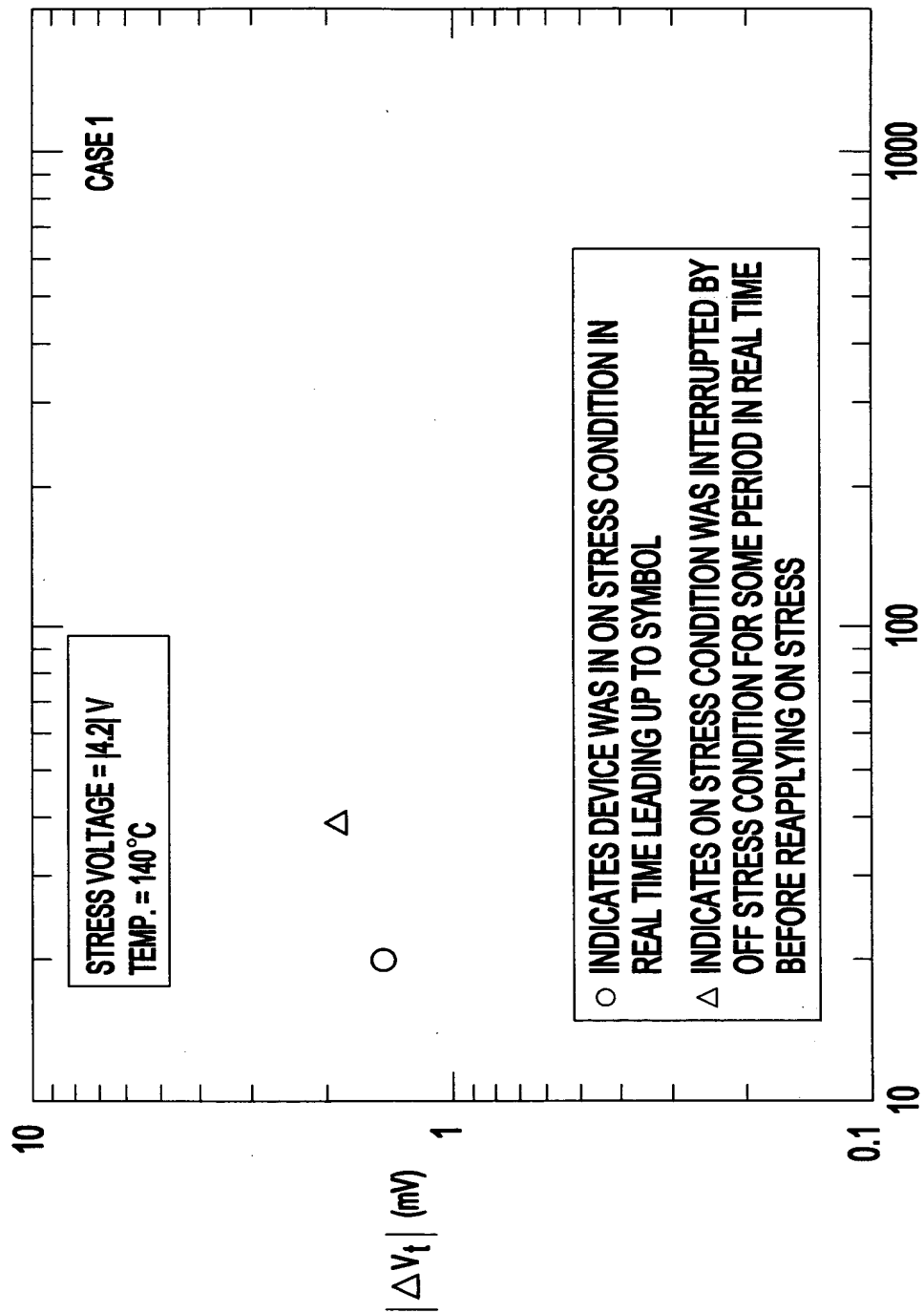


FIG.7B

EFFECTIVE TIME UNDER 'ON' STRESS CONDITION (s)

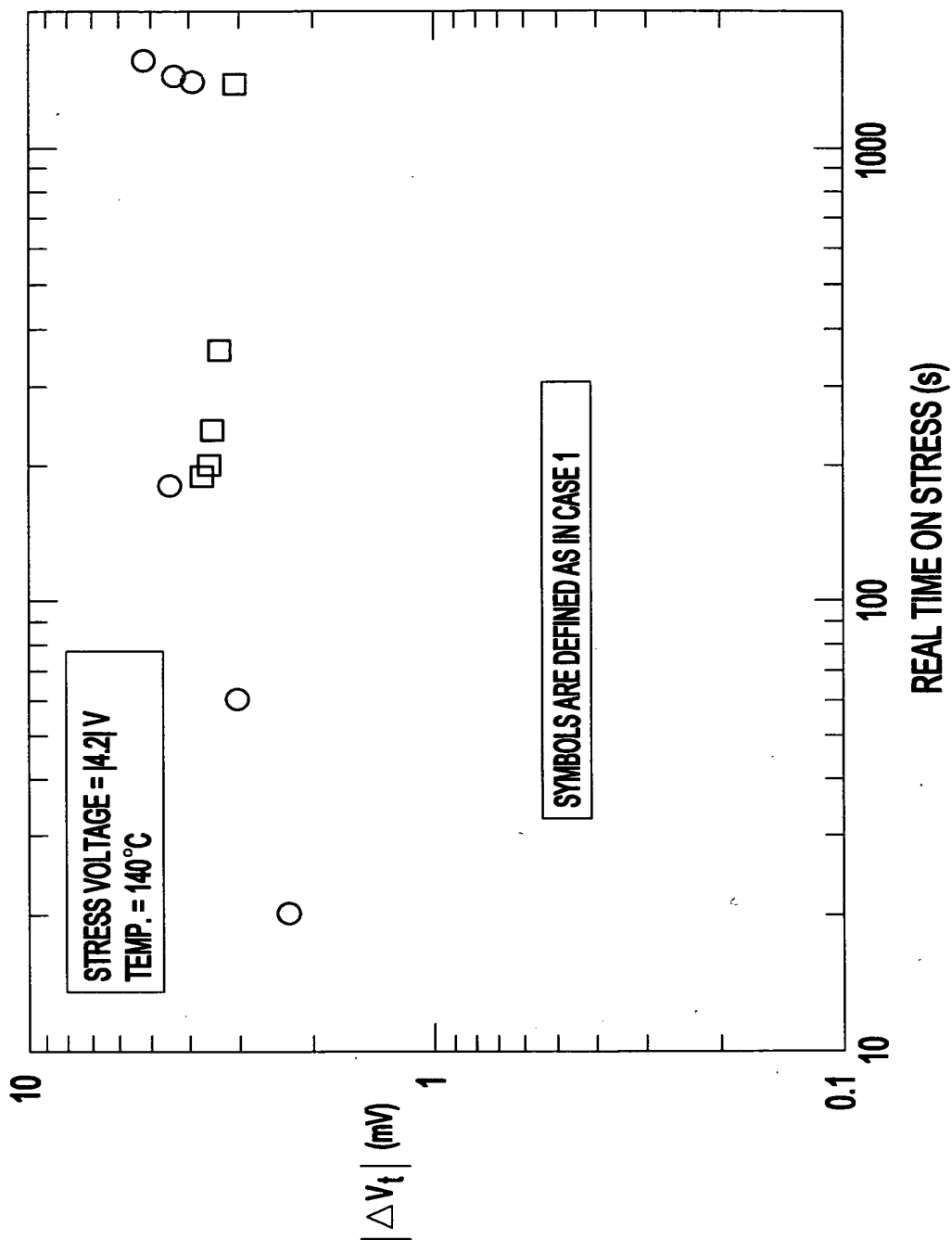


FIG.8A

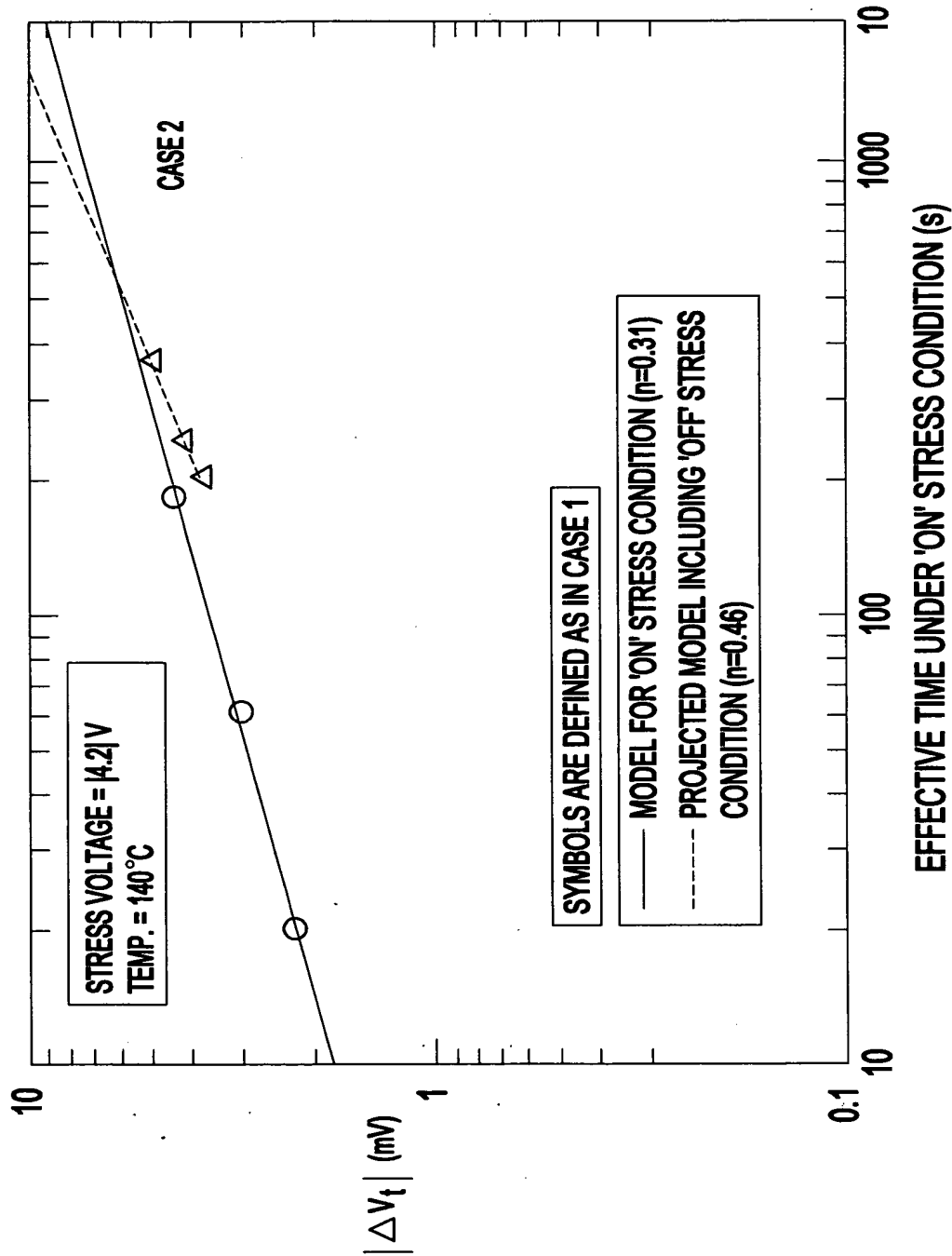


FIG. 8B

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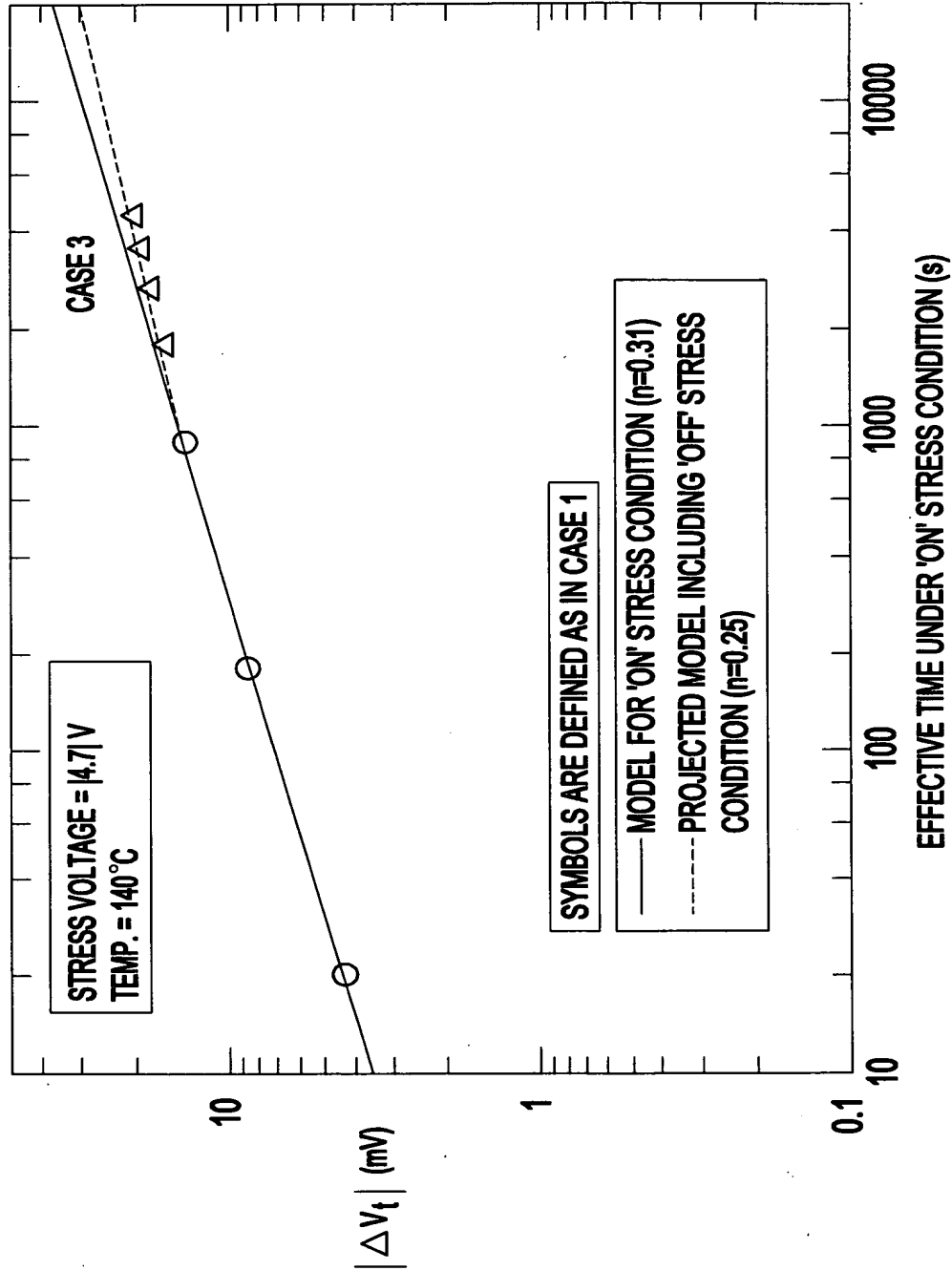
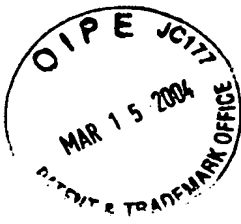


FIG. 9B



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REPLACEMENT SHEET

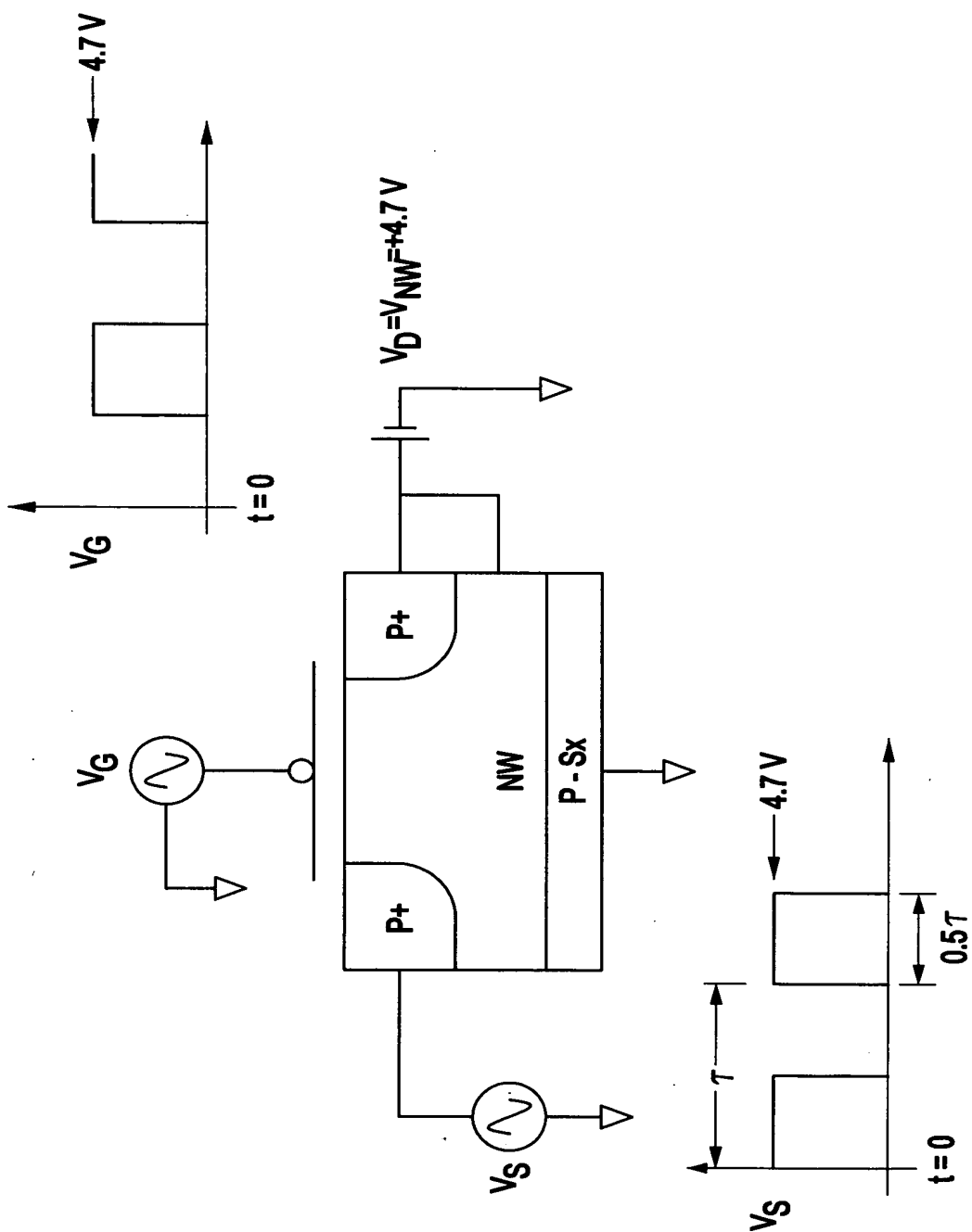


FIG.10

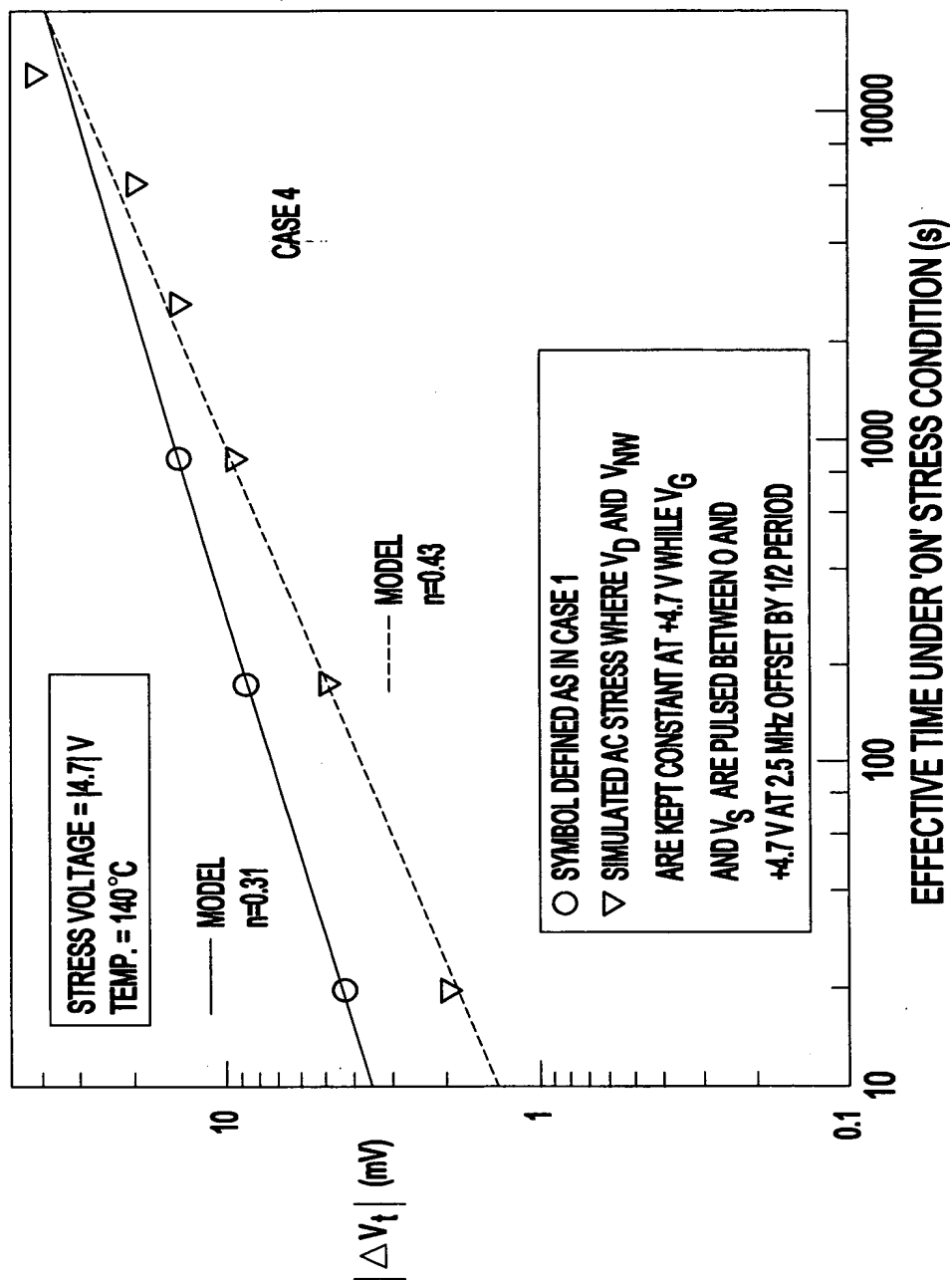


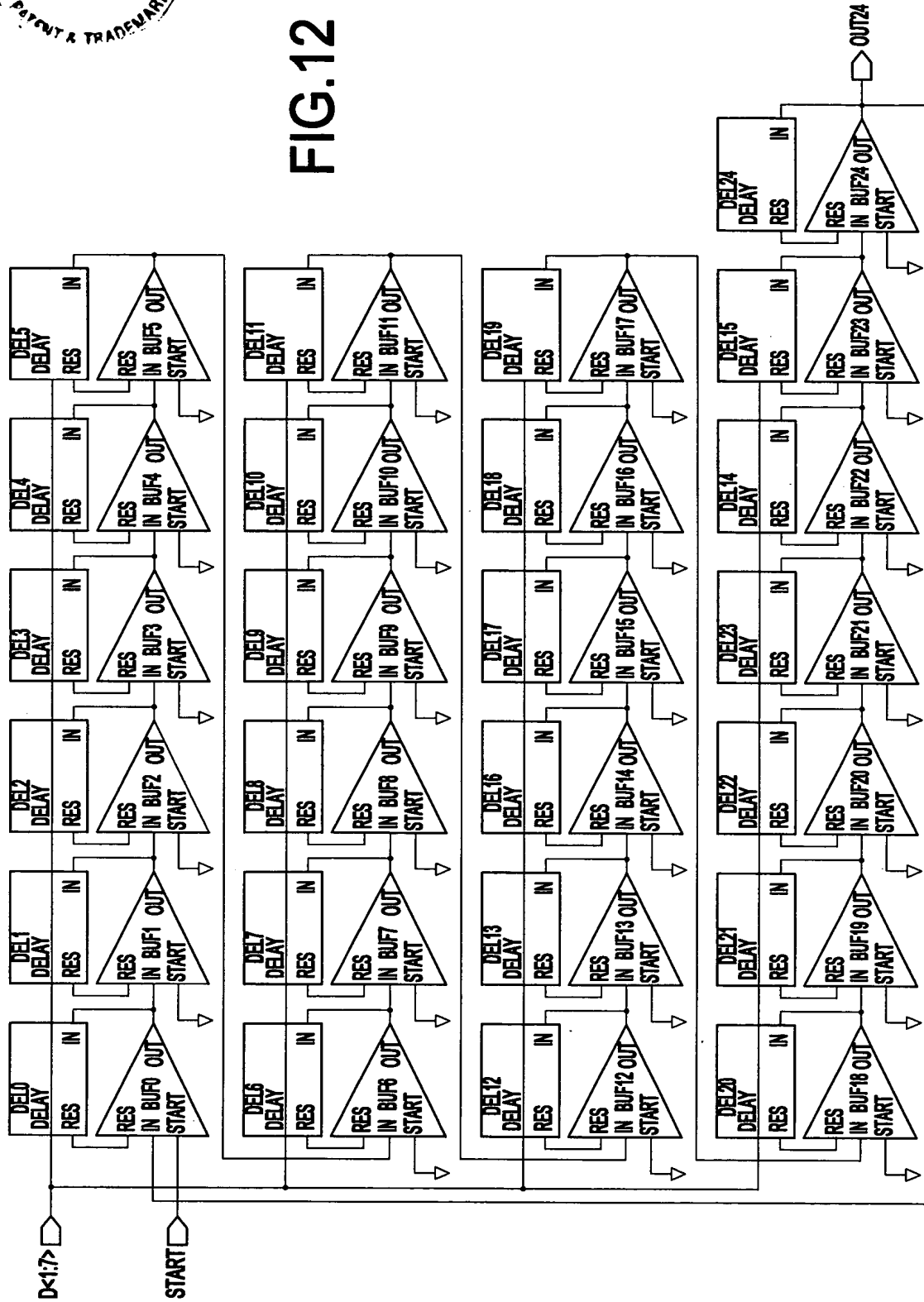
FIG.11



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REPLACEMENT SHEET

FIG.12





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REPLACEMENT SHEET

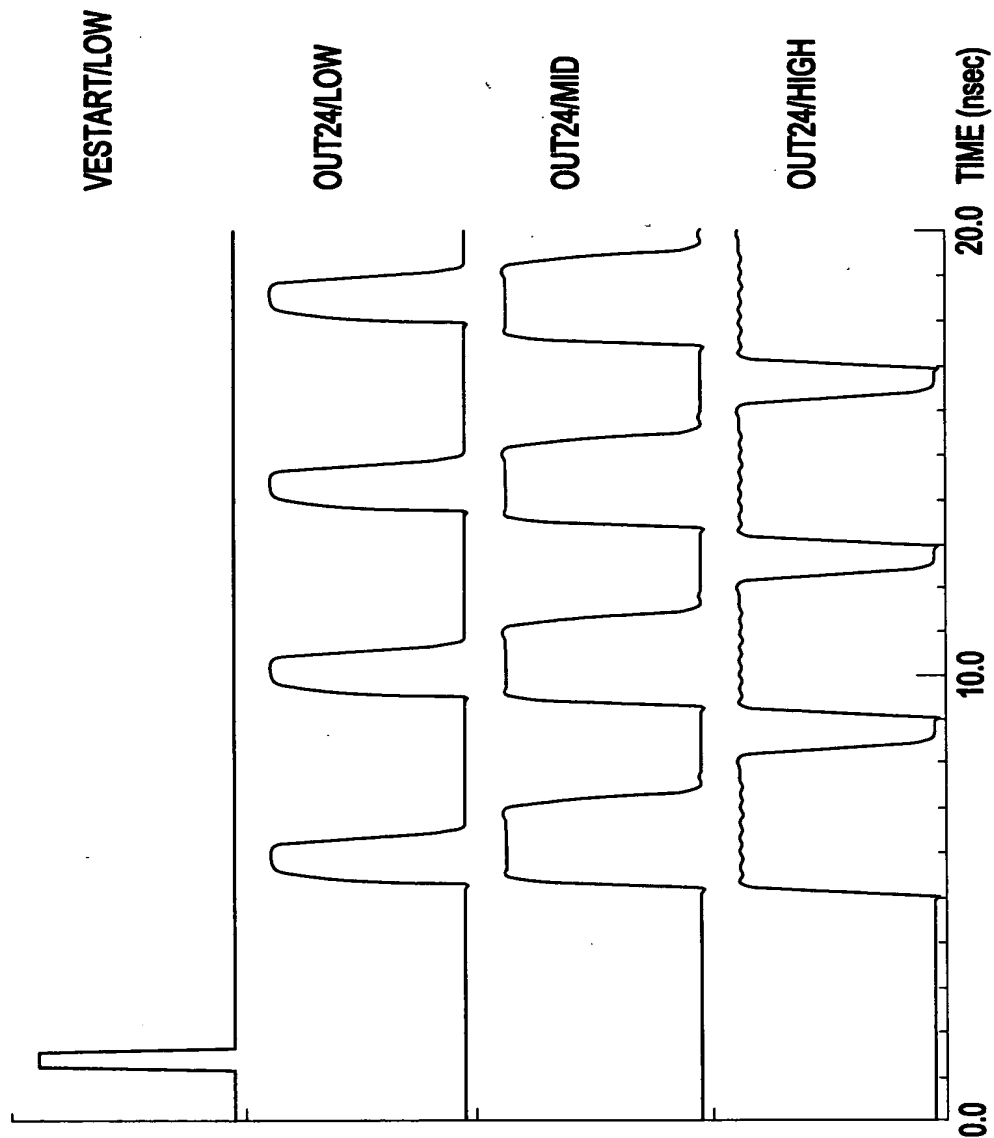


FIG.13



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REPLACEMENT SHEET

DVTSBUF
PULSE PROPAGATOR STRUCTURE

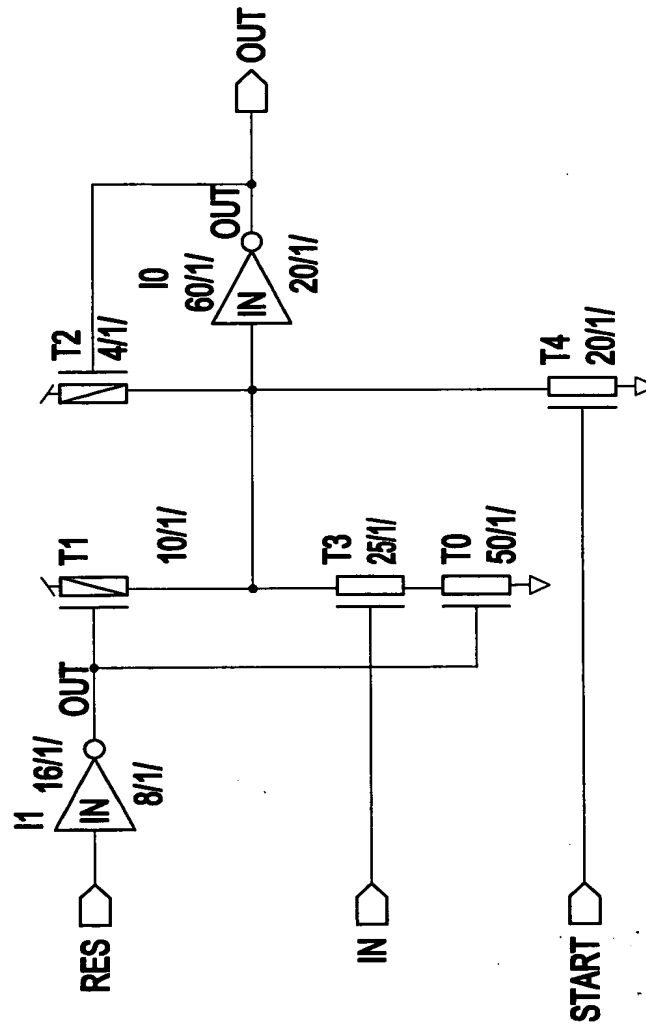


FIG.14

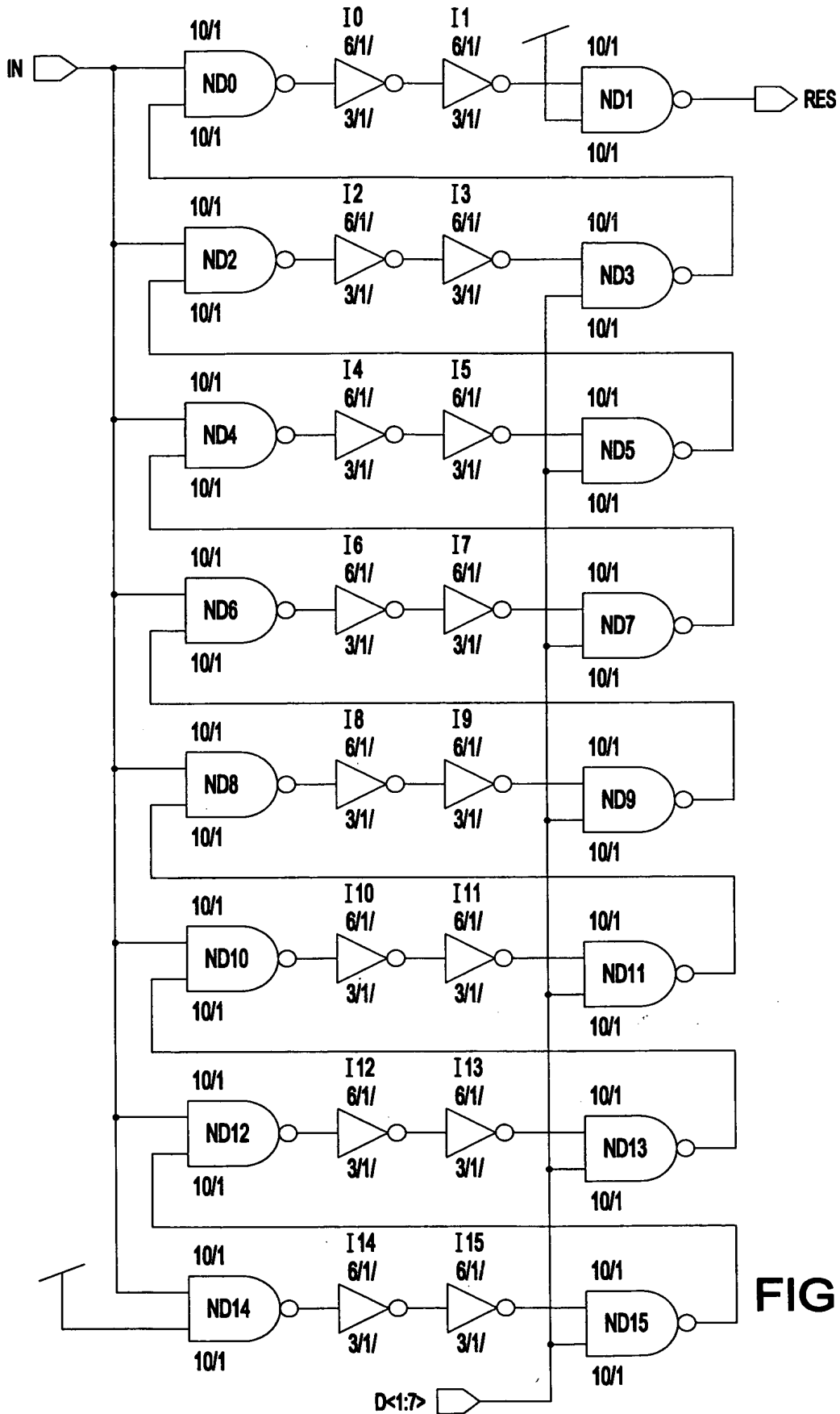
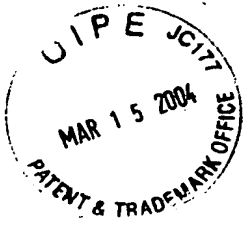


FIG.15





INPUT	FREE RUN	AC STRESS	DC STRESS
SOURCE	VDD	VDD	VDD/GND
GATE	GND	"A.C."	VDD/GND
VPP	VPP	VPP	VPP
bSTRESS1	VDD	VDD	GND
IN	"A.C."	GND	GND
DRAIN	VDD	VDD	VDD/GND
bSTRESS2	VPP	GND	GND

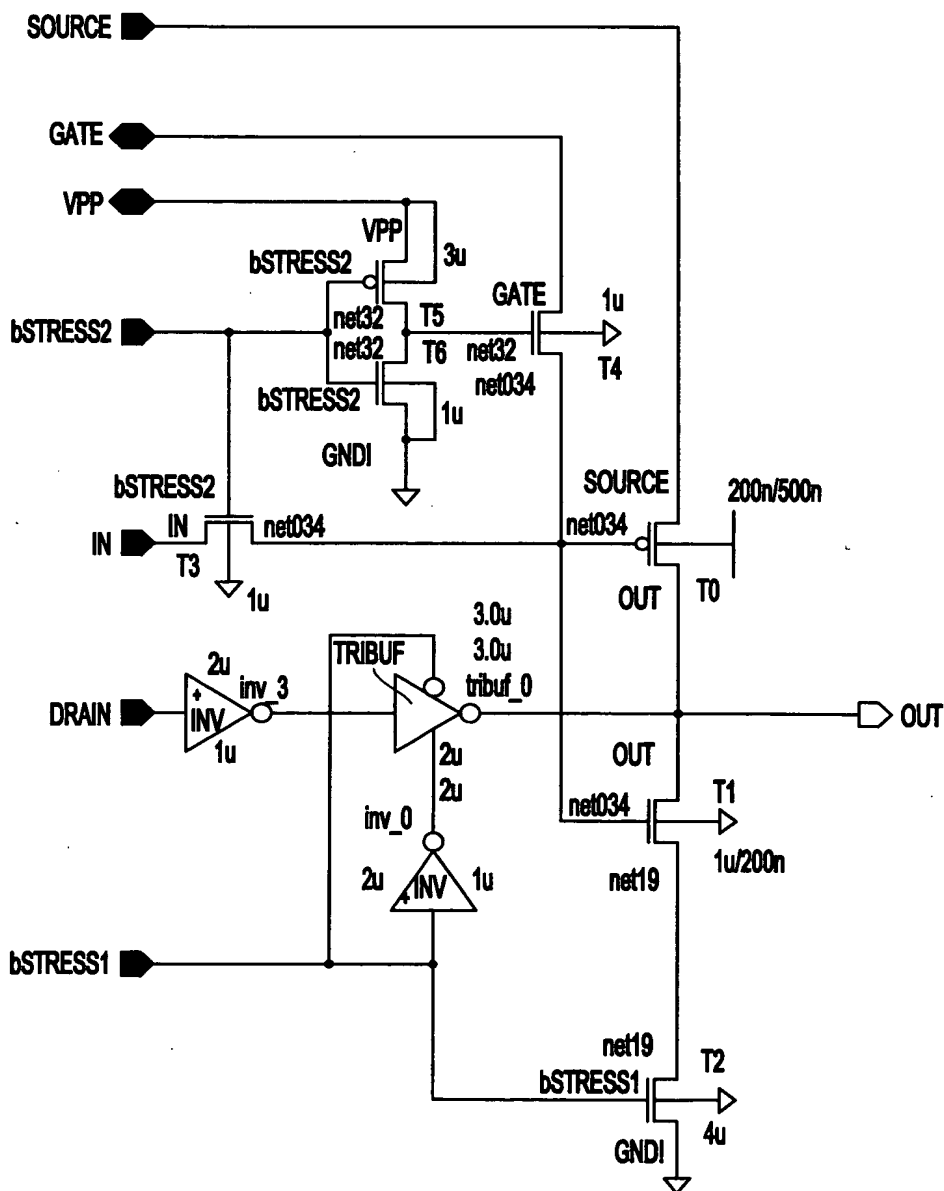


FIG.16

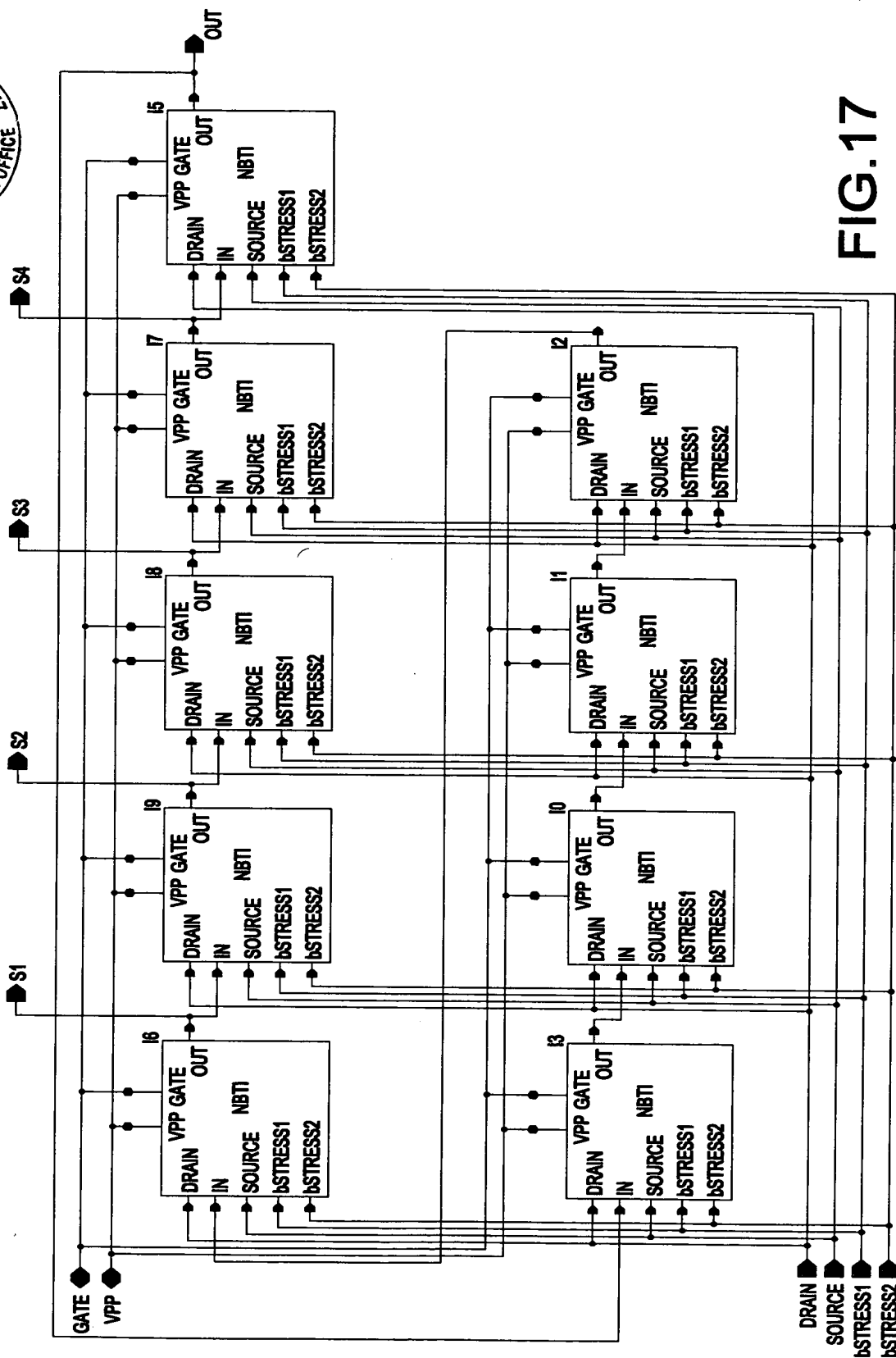
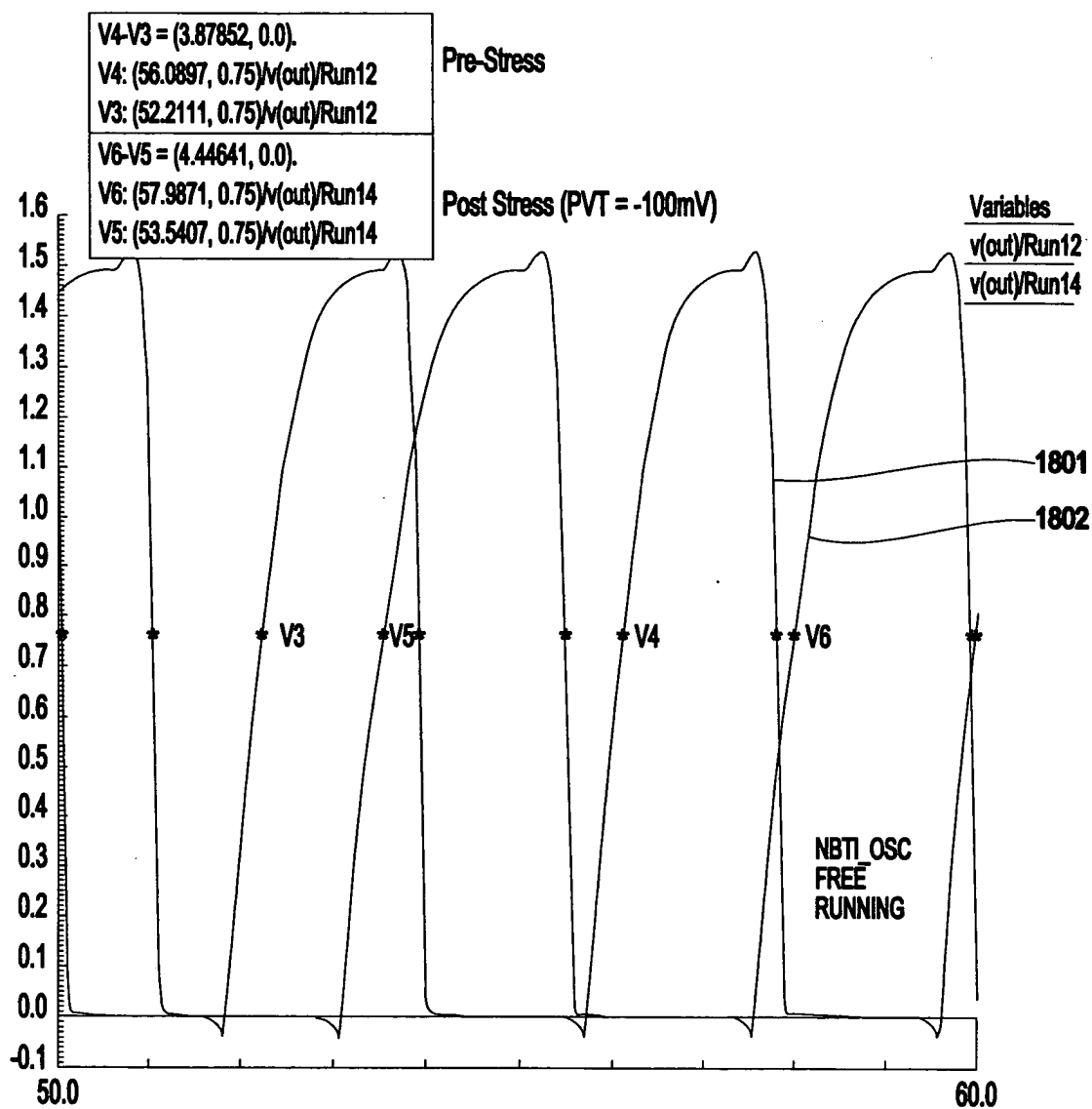
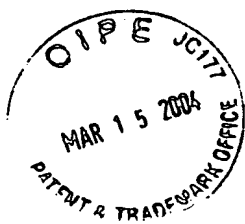


FIG.17

**FIG.18**

**FIG.19**

APPLICATION OF PFET DC STRESS IN RING OSCILLATOR
AND DETERMINING DEGRADATION CONSTANTS

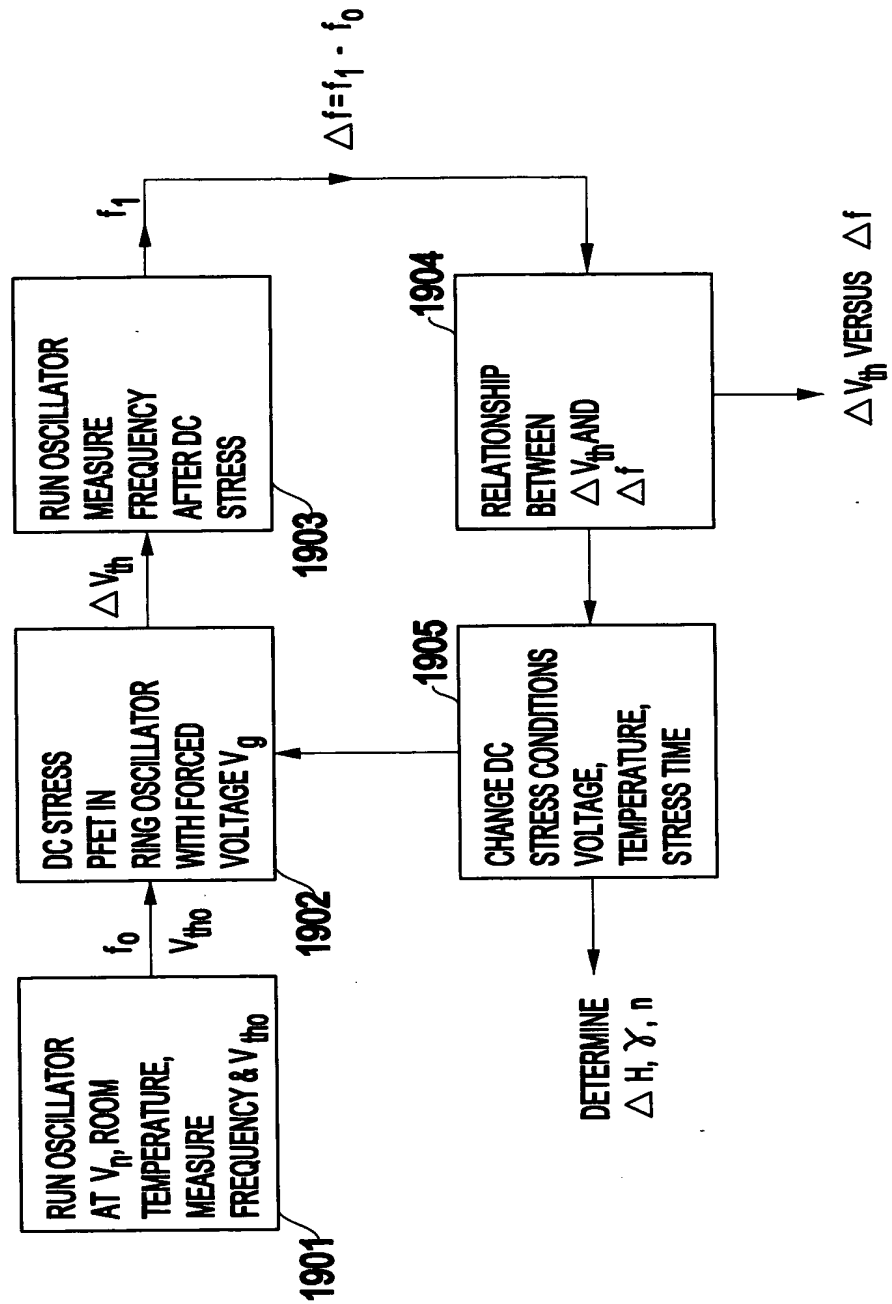


FIG.20

